

FIG. 1

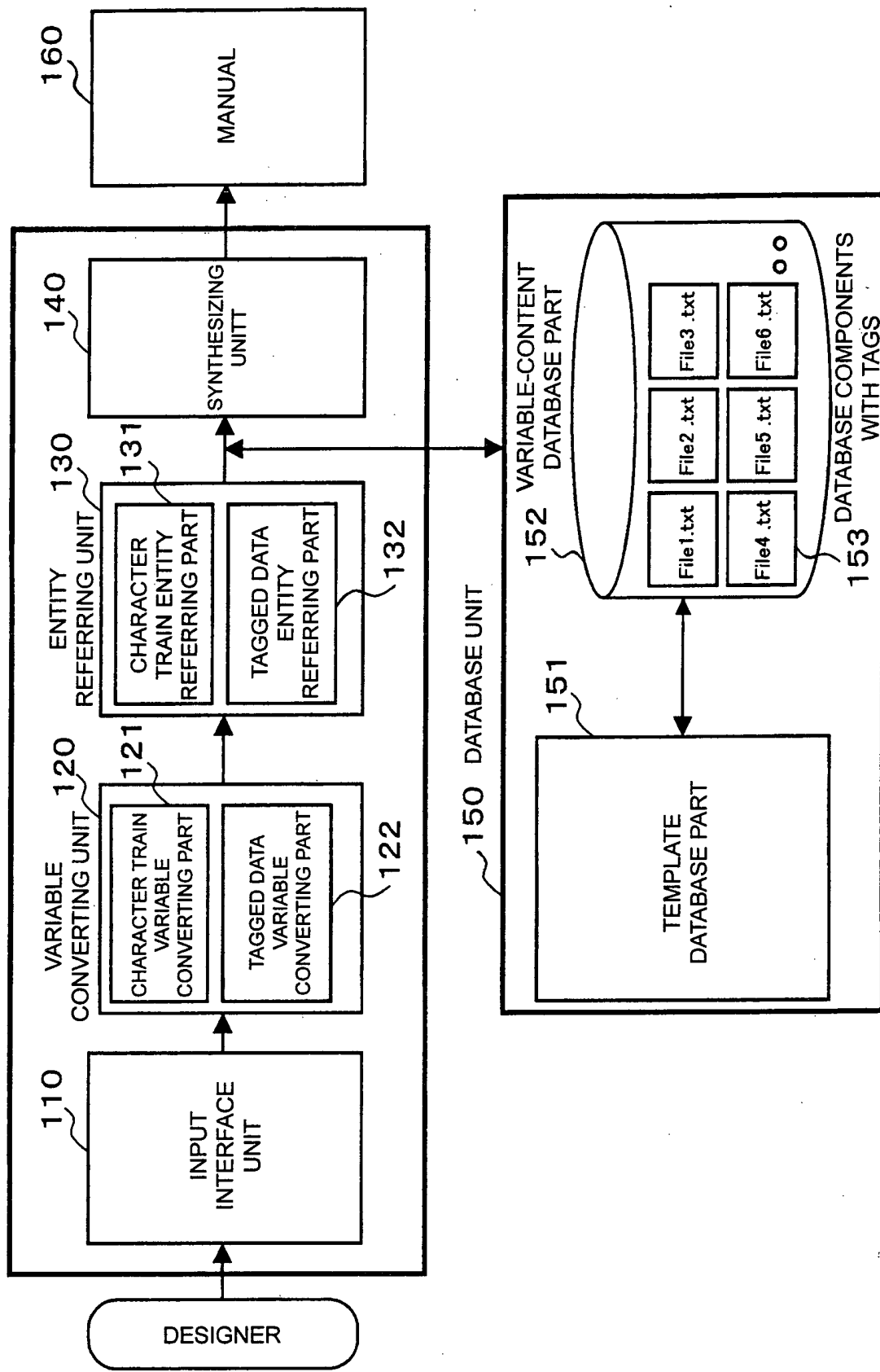
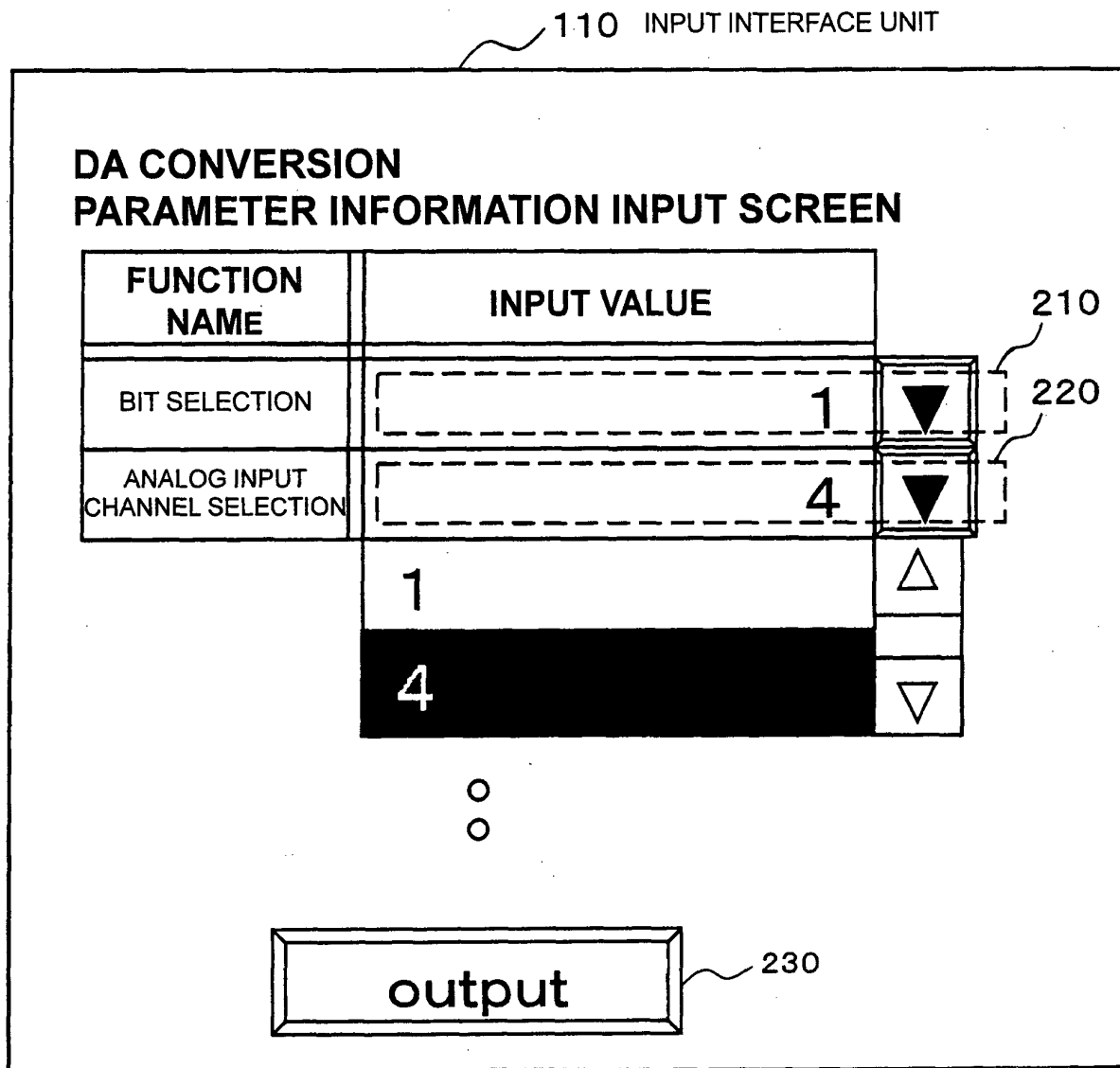


FIG. 2



# FIG. 3

120 VARIABLE CONVERTING UNIT

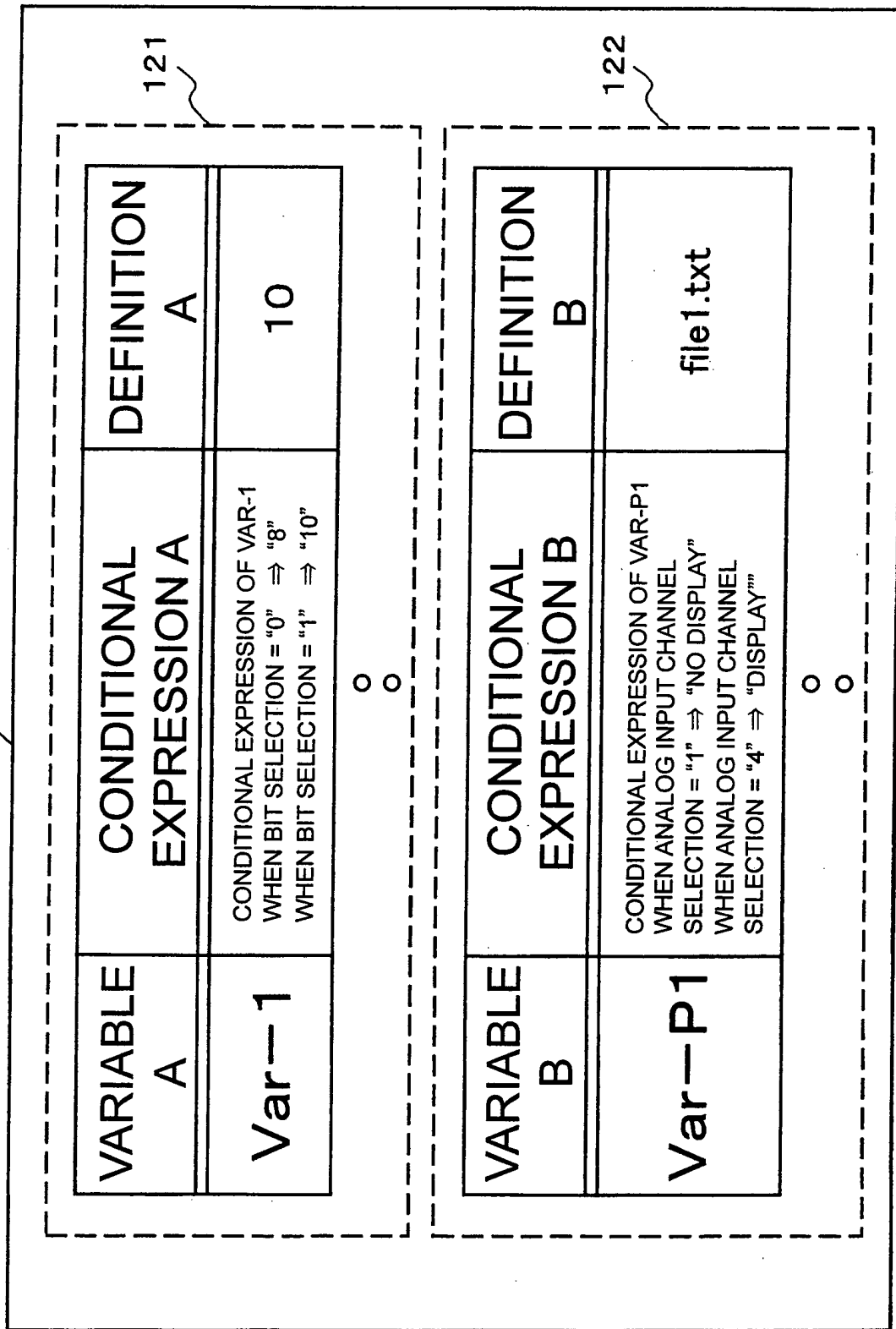
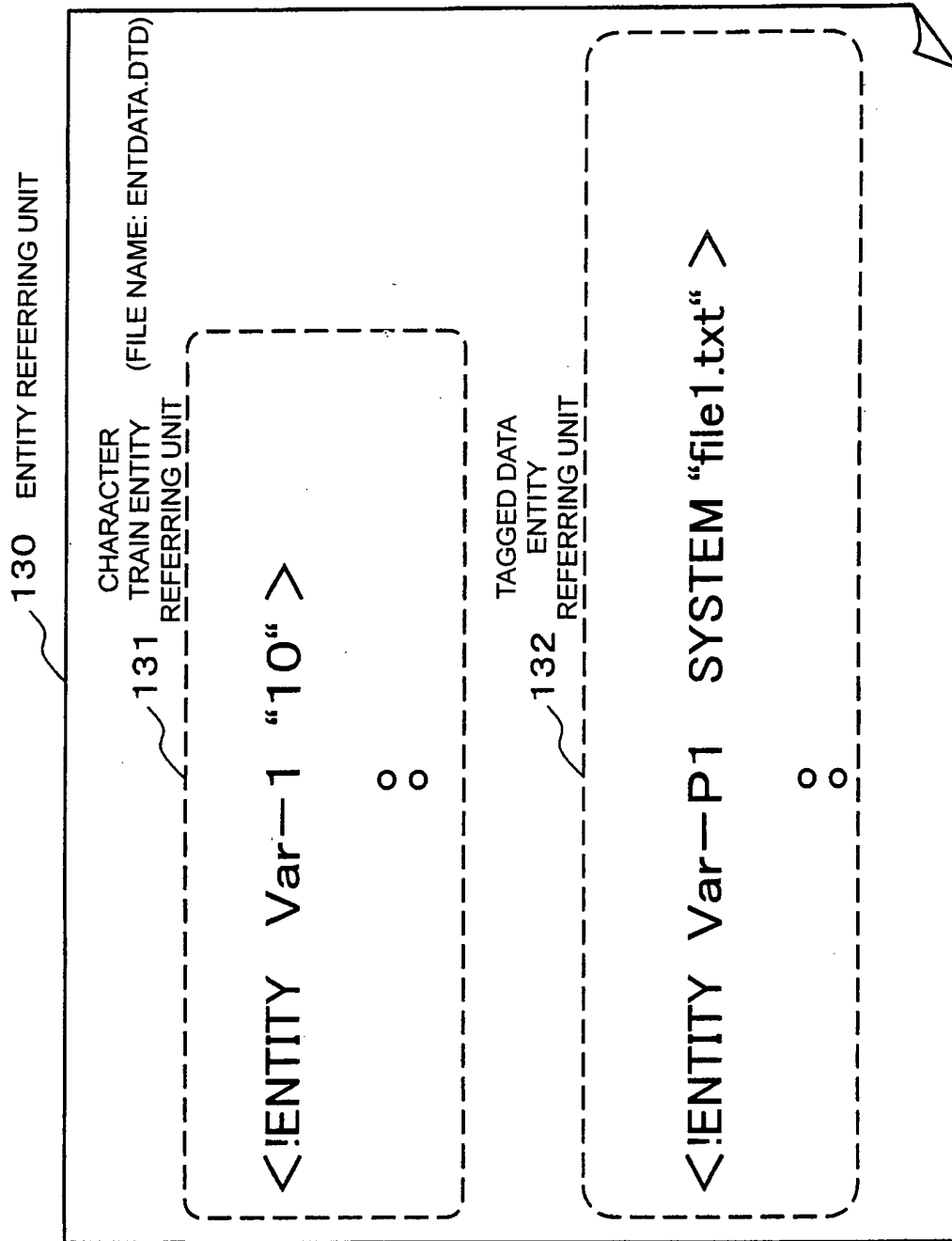
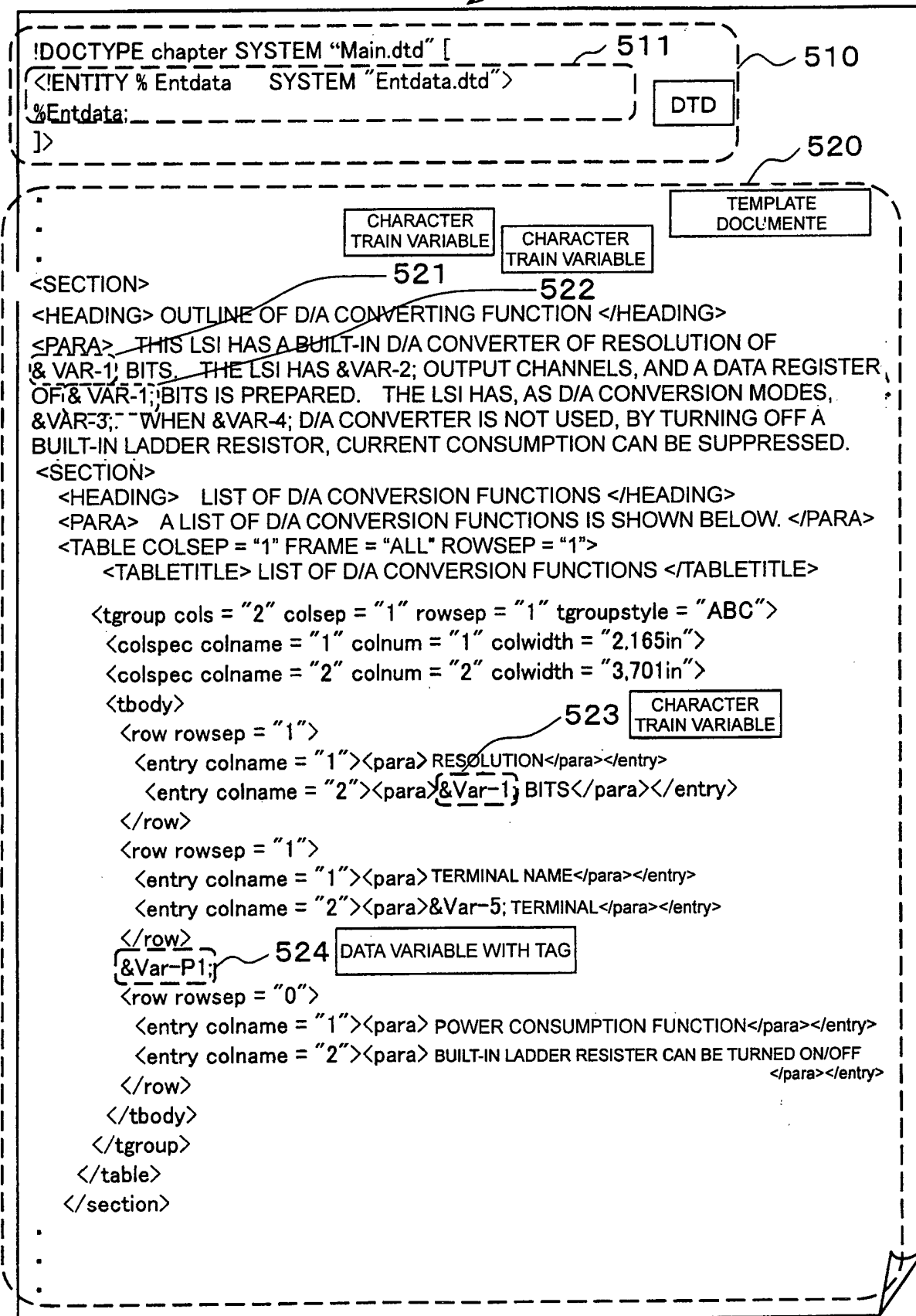


FIG. 4



# FIG. 5

151 TEMPLATE/DATABASE PART



# FIG. 6

153 DATABASE COMPONENT WITH TAG

(FILE NAME: FILE1.TXT) FILE CORRESPONDING TO VAR-P1

```

<row rowsep = "1">
  <entry colname = "1"><para> D/A CONVERSION CHANNEL MONITOR</para></entry>
  <entry colname = "2"><para> CHANNEL 0 TO CHANNEL 3</para>
</entry>
</row>
<row rowsep = "1">
  <entry colname = "1"><para> SCAN CLOCK SELECTION</para></entry>
  <entry colname = "2"><para> fs/16, fs/32, fs/64, fs/128</para></entry>
</row>
<row rowsep = "1">
  <entry colname = "1"><para> D/A CONVERSION MODE SELECTION</para></entry>
  <entry colname = "2"><para> FIXED CONVERSION MODE, 2-CHANNEL CONVERSION, 4-CHANNEL
    CONVERSION</para></entry>
</row>

```

# FIG. 7

160 MANUAL

## 1.1 OUTLINE OF D/A CONVERSION FUNCTIONS

ENTITY OF  
CHARACTER  
TRAIN VARIABLE

710

711

ENTITY OF CHARACTER  
TRAIN VARIABLE

THIS LSI HAS A BUILT-IN D/A CONVERTER OF RESOLUTION OF 10 BITS. THE LSI HAS 4 OUTPUT CHANNELS, AND A DATA REGISTER OF 10 BITS IS PREPARED. THE LSI HAS, AS D/A CONVERSION MODES, A CHANNEL FIXED CONVERSION MODE, A 2-CHANNEL FIXED CONVERSION MODE, AND A 4-CHANNEL CONVERSION MODE. IN THE 2-CHANNEL CONVERSION MODE OR 4-CHANNEL CONVERSION MODE, THE CHANNEL CAN BE CHANGED. WHEN D/A CONVERTER IS NOT USED, BY TURNING OFF A BUILT-IN LADDER RESISTOR, CURRENT CONSUMPTION CAN BE SUPPRESSED.

### 1.1.1 LIST OF D/A CONVERSION FUNCTIONS

A LIST OF D/A CONVERSION FUNCTIONS IS SHOWN BELOW.

712

ENTITY OF CHARACTER  
TRAIN VARIABLE

RESOLUTION	10 BITS
TERMINAL NAME	DADR31 TO DADR00 TERMINALS
D/A CONVERSION CHANNEL MONITOR	CHANNELS 0 TO 3
SCAN CLOCK SELECTION	fs/16, fs/32, fs/64, fs/128
D/A CONVERSION MODE SELECTION	FIXED CONVERSION MODE, 2-CHANNEL CONVERSION, 4-CHANNEL CONVERSION
POWER CONSUMPTION FUNCTION	BUILT-IN LADDER RESISTOR CAN BE TURNED ON/OFF

720

ENTITY OF DATA VARIABLE WITH  
TAG

FIG. 8

